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Applicant: Charles W.C. Lin

Title: SUPPORT CIRCUIT WITH A TAPERED THROUGH-HOLE
FOR A SEMICONDUCTOR CHIP ASSEMBLY

Serial No.: 10/002,732 Filed: November 15, 2001

Examiner: Unknown Group Art Unit: Unknown

Atty. Docket No.: P012-1

#4
41-03
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ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

TRANSMITTAL OF FORMAL DRAWINGS

Attached please find the formal drawings (17 sheets) for this application.

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David M. Sigmond
Attorney for Applicant

12/17/01
Date of Signature

Respectfully submitted,

David M. Sigmond
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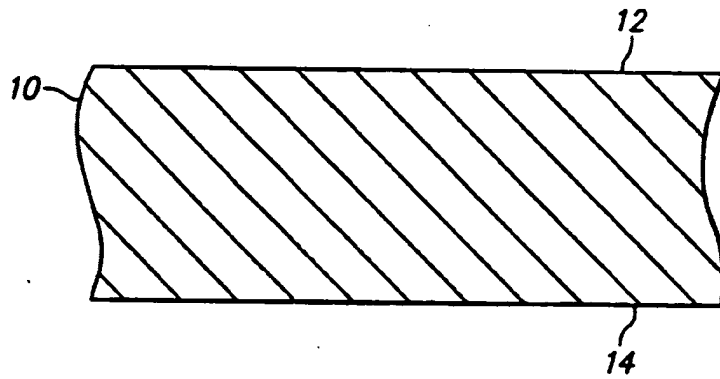


FIG. 1A

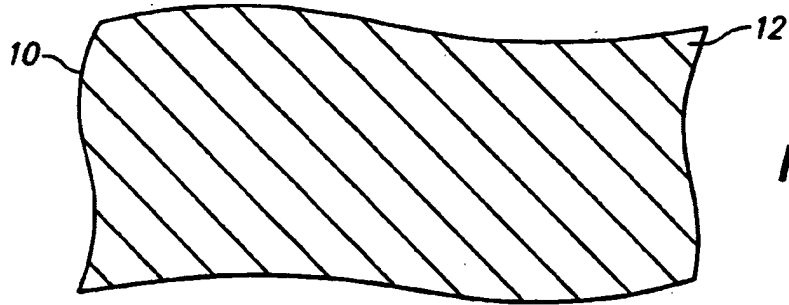


FIG. 2A

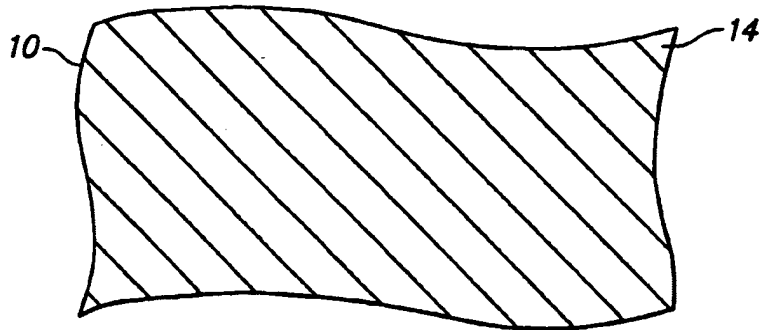


FIG. 3A

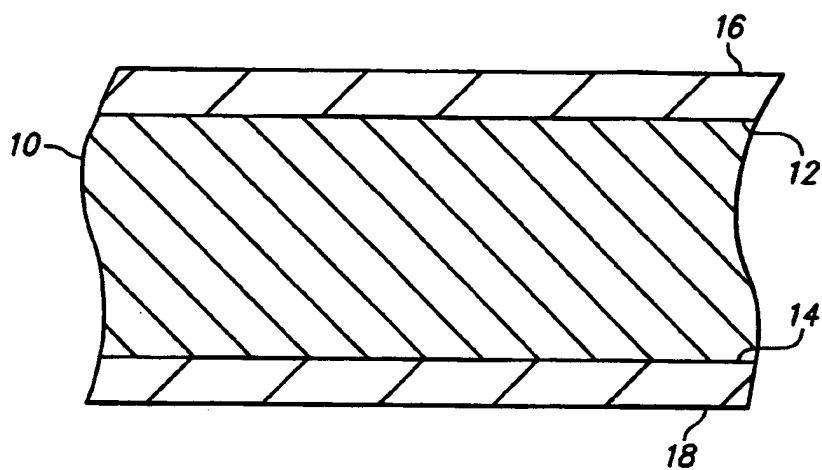


FIG. 1B

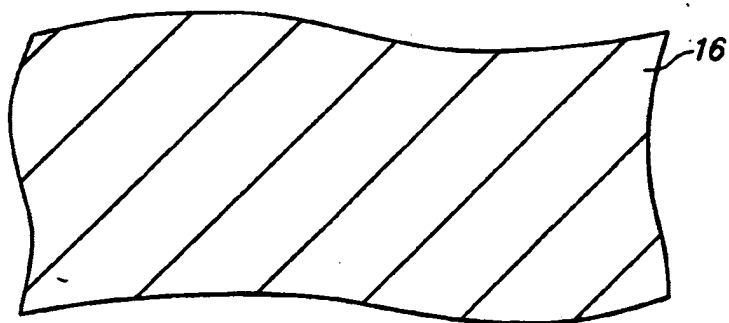


FIG. 2B

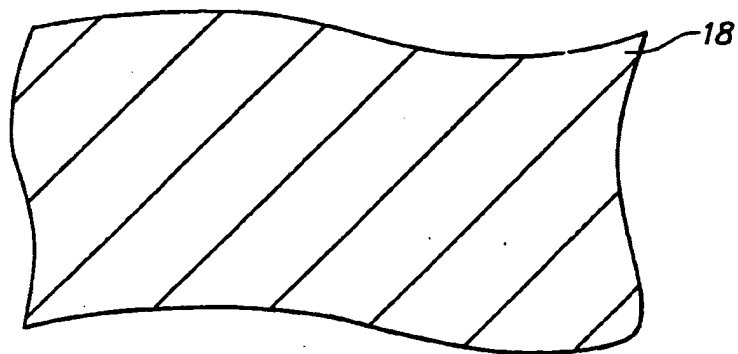


FIG. 3B

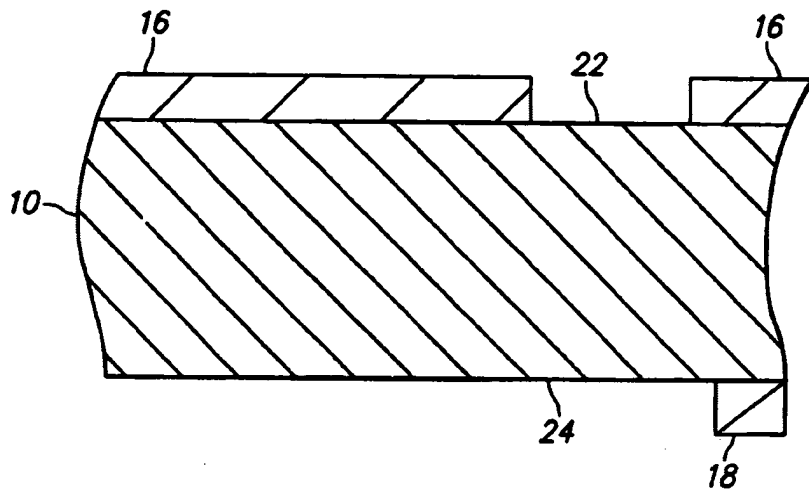


FIG. 1C

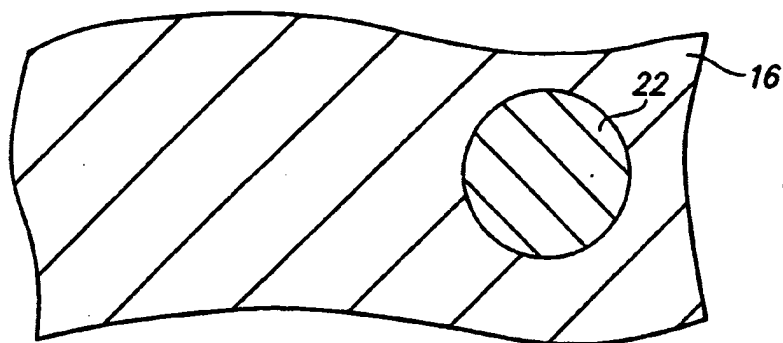


FIG. 2C

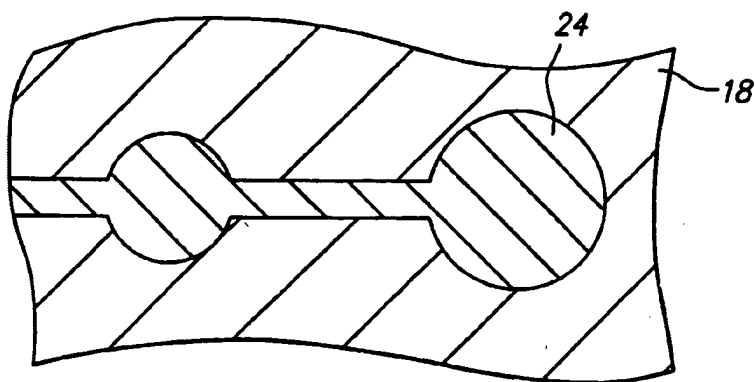


FIG. 3C

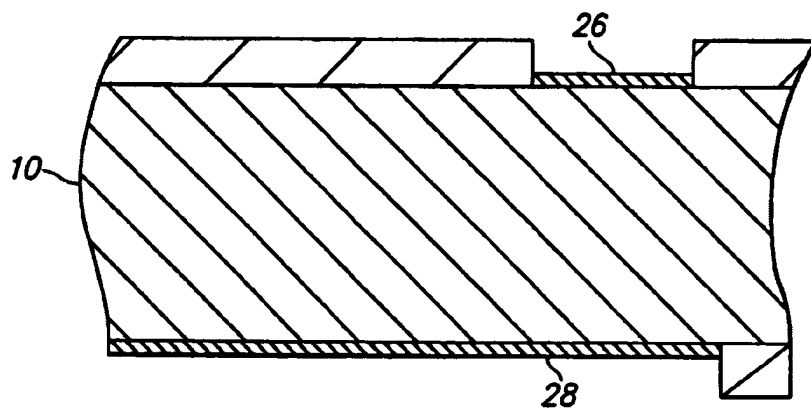


FIG. 1D

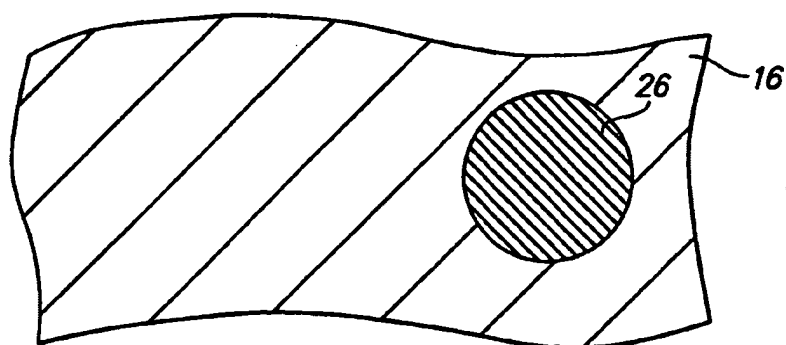


FIG. 2D

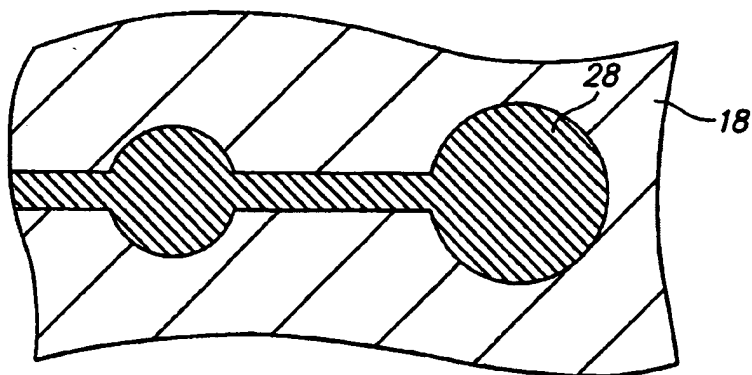


FIG. 3D

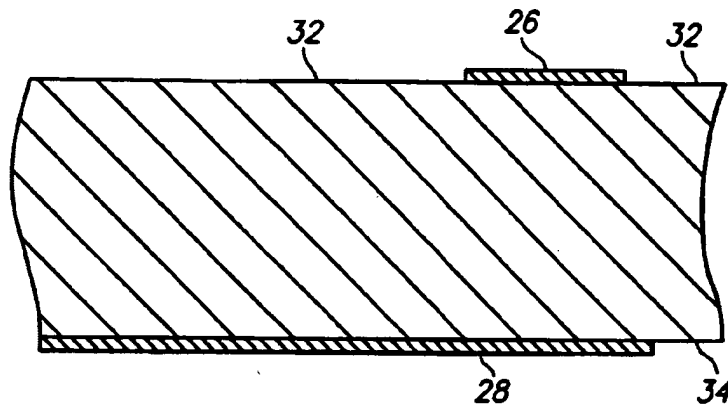


FIG. 1E

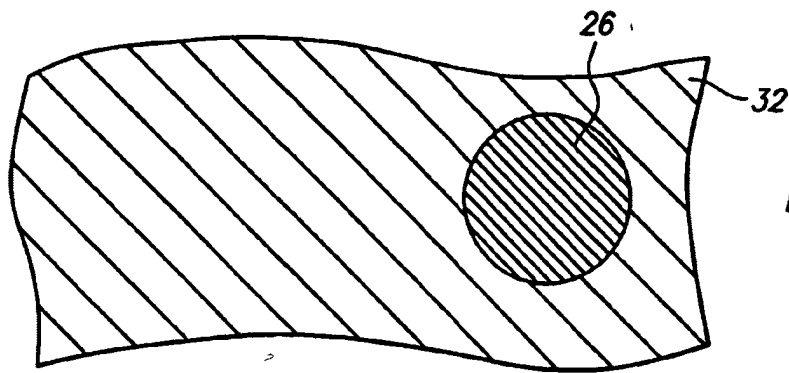


FIG. 2E

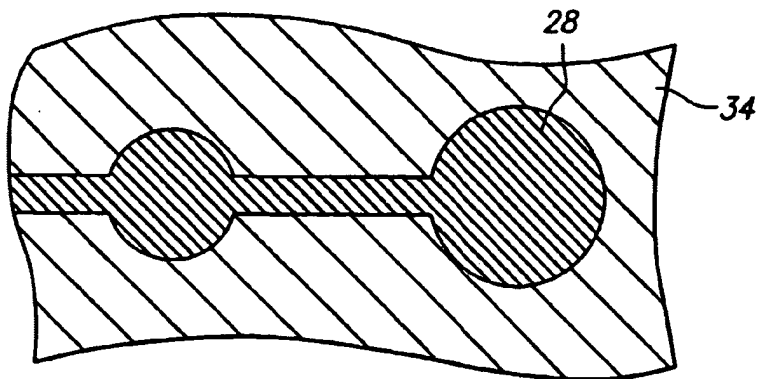


FIG. 3E

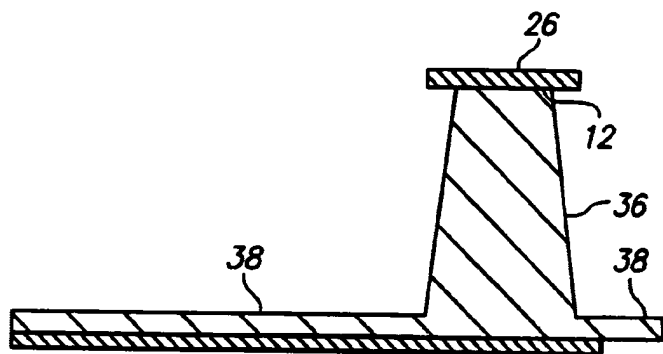


FIG. 1F

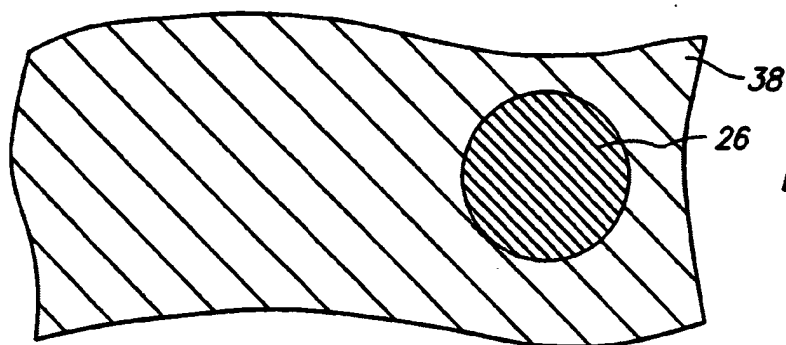


FIG. 2F

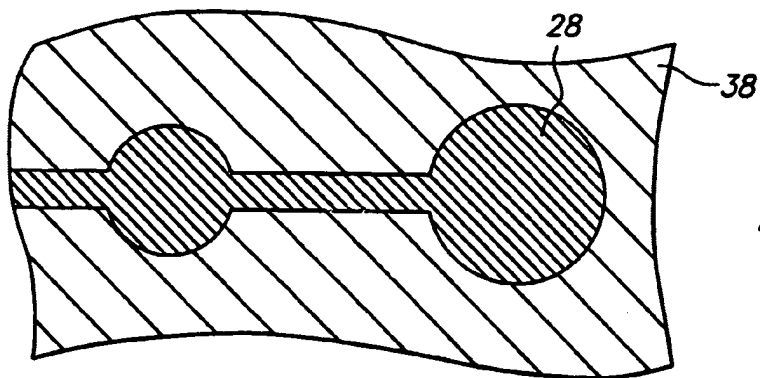


FIG. 3F

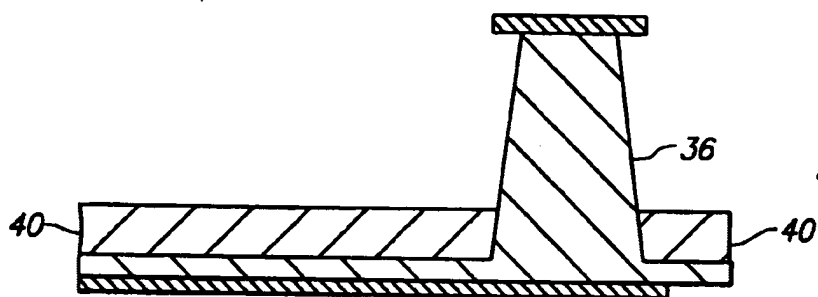


FIG. 1G

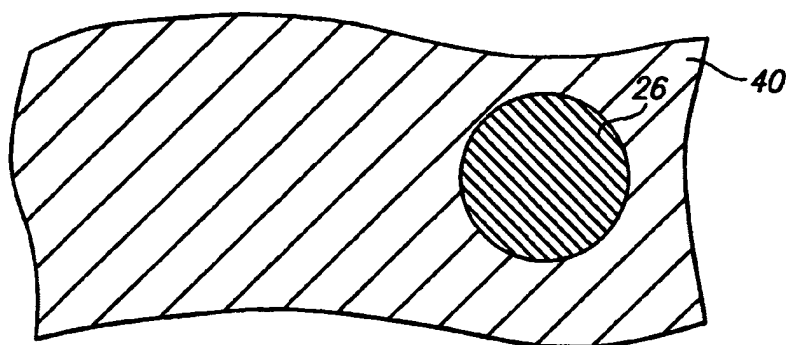


FIG. 2G

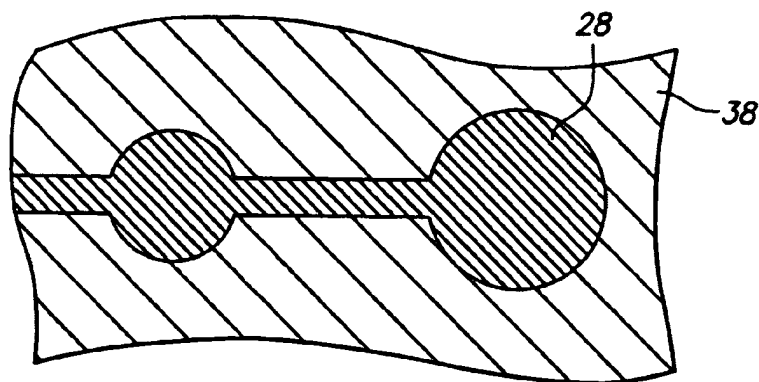


FIG. 3G

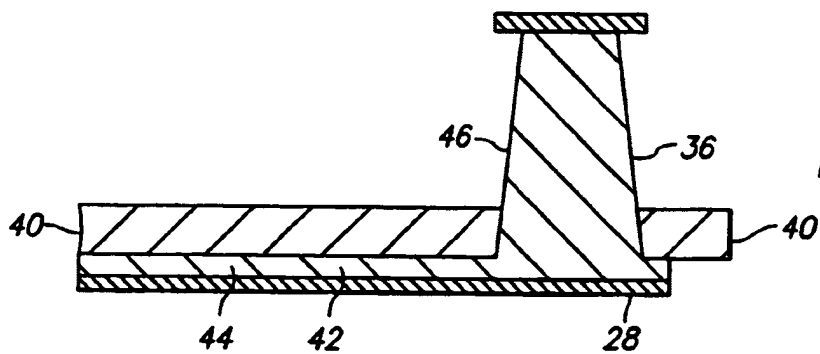


FIG. 1H

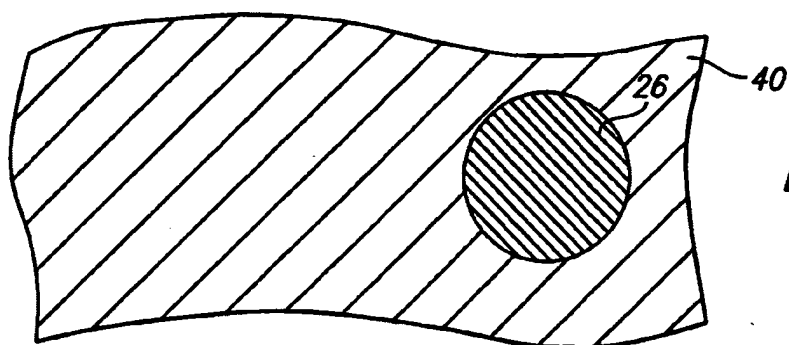


FIG. 2H

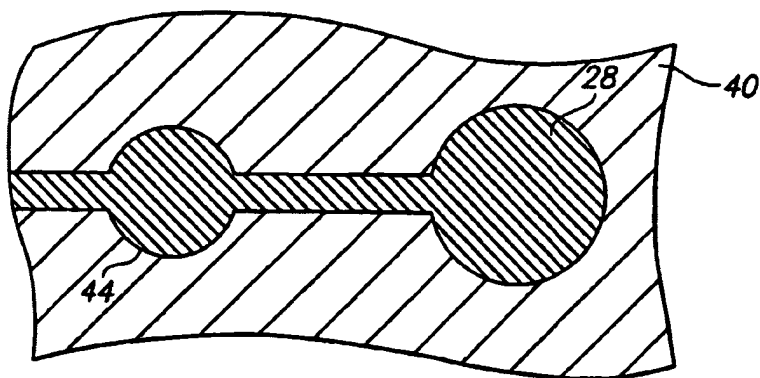


FIG. 3H

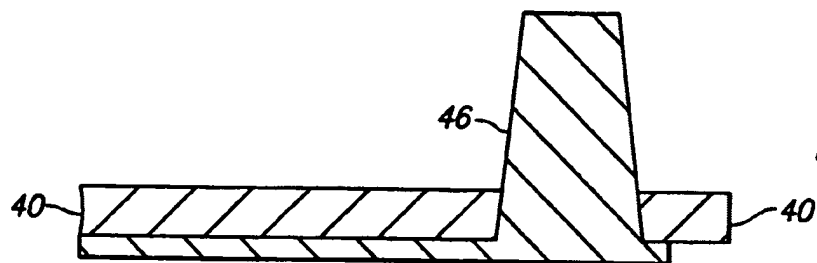


FIG. 1

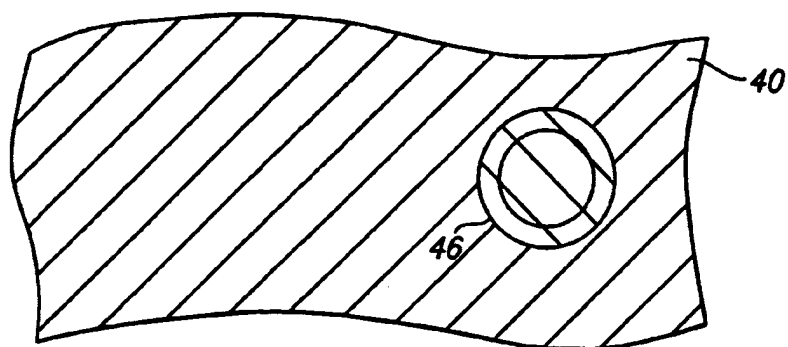


FIG. 2

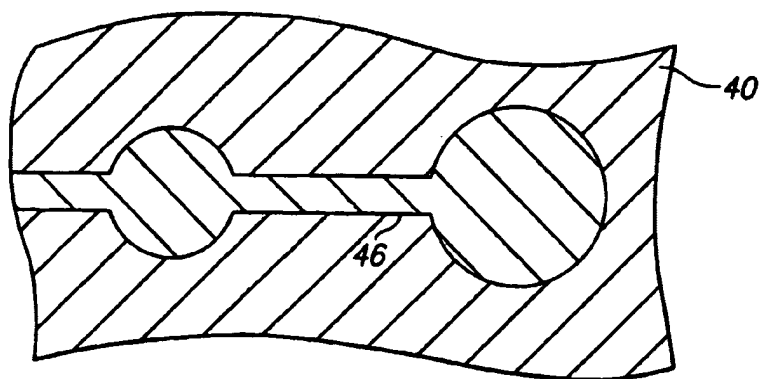


FIG. 3

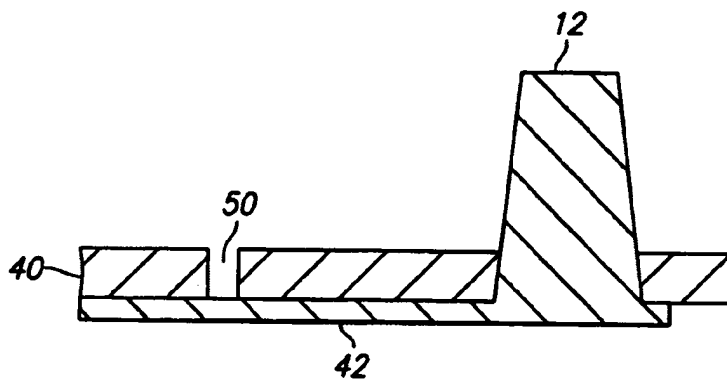


FIG. 1J

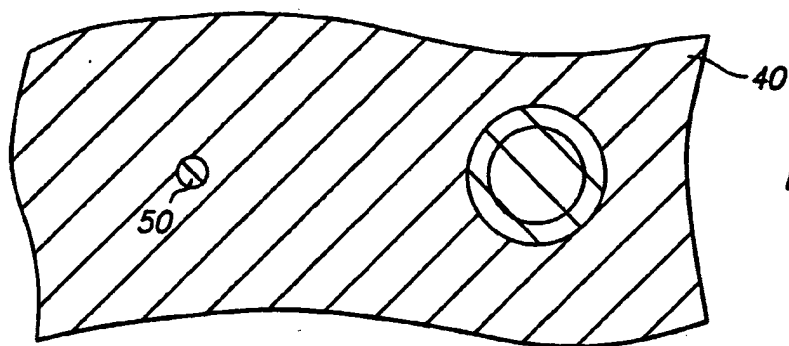


FIG. 2J

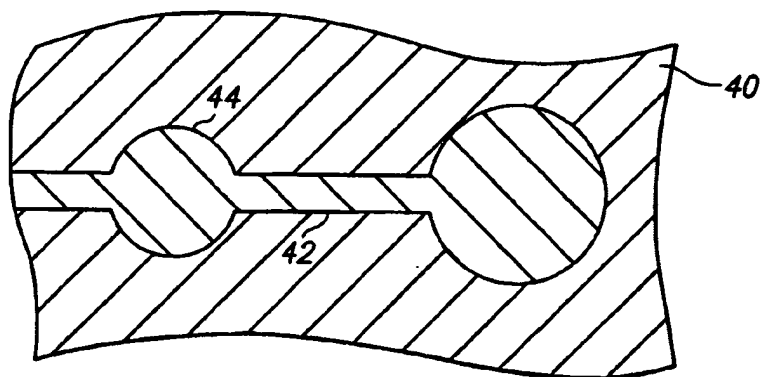


FIG. 3J

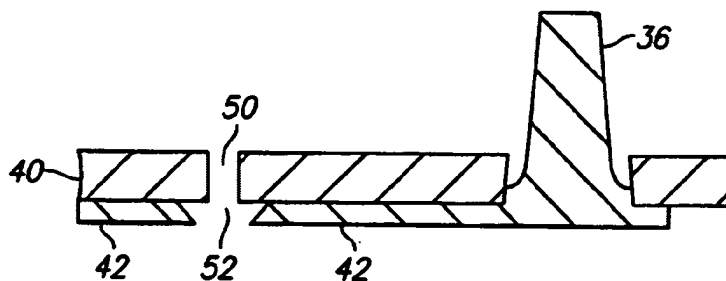


FIG. 1K

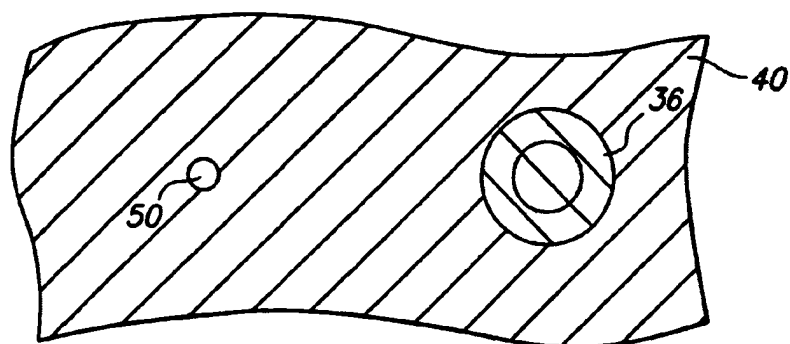


FIG. 2K

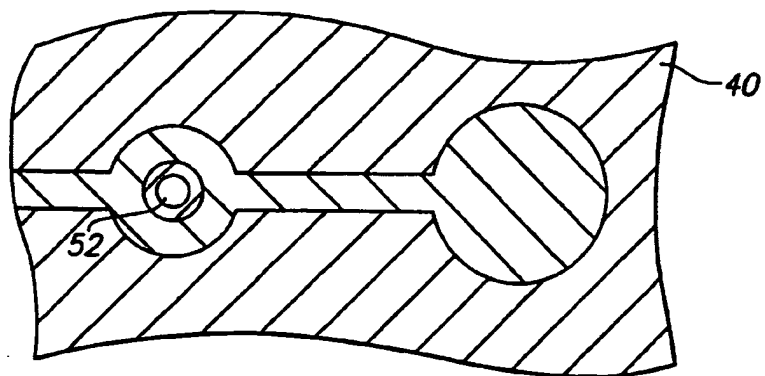


FIG. 3K

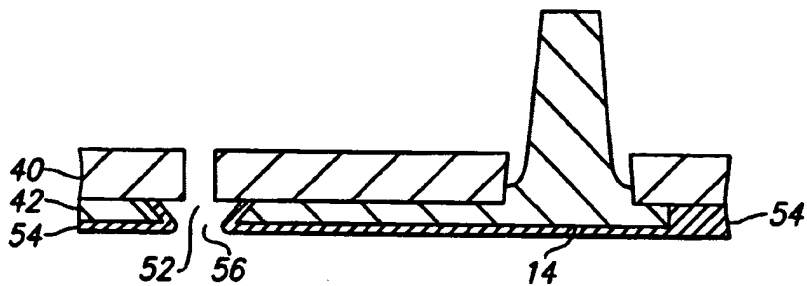


FIG. 1L

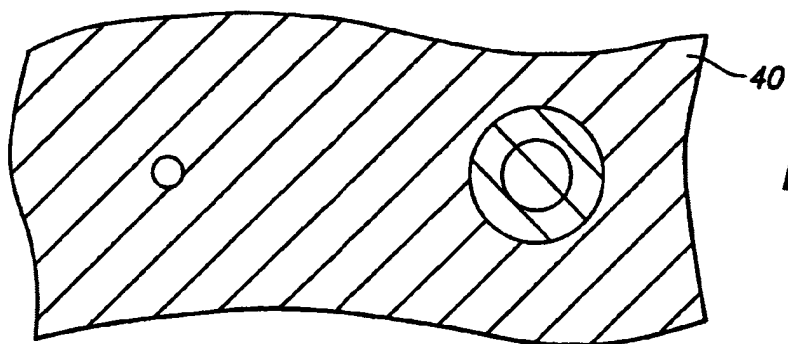


FIG. 2L

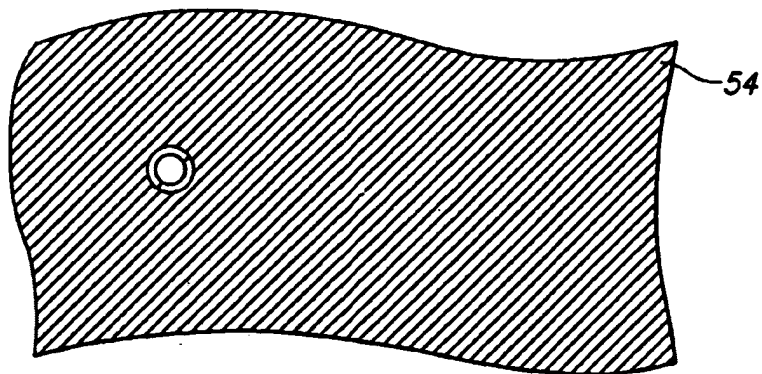


FIG. 3L

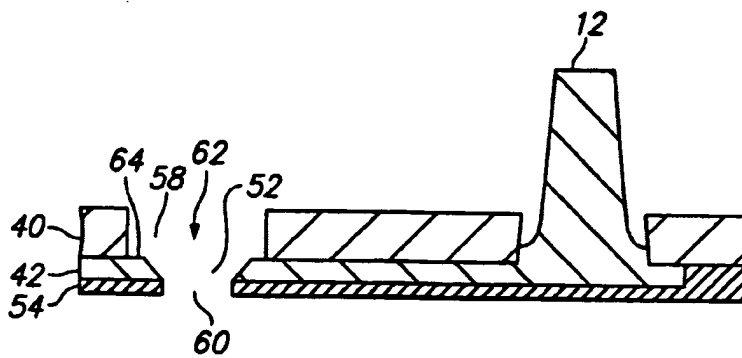


FIG. 1M

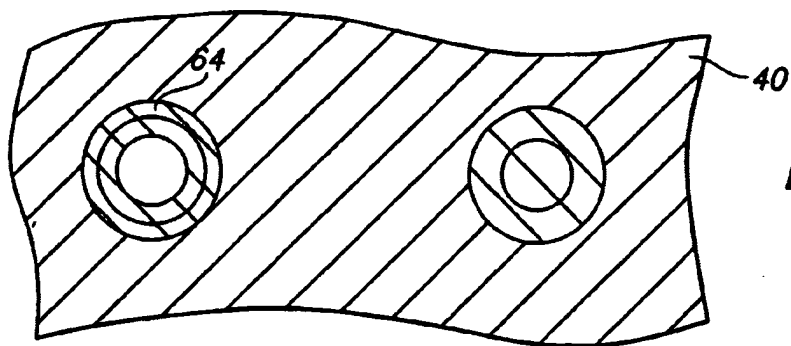


FIG. 2M

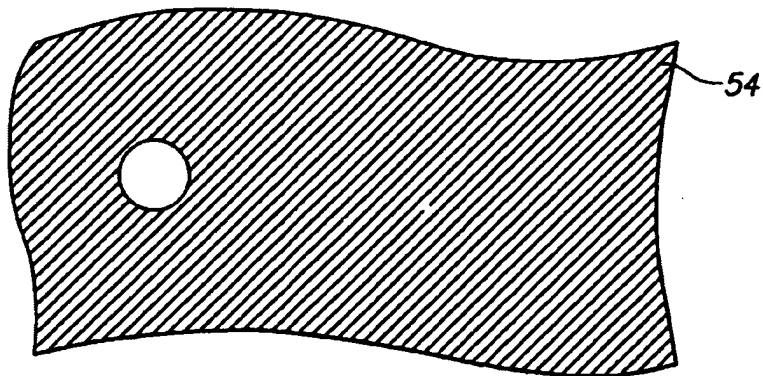


FIG. 3M

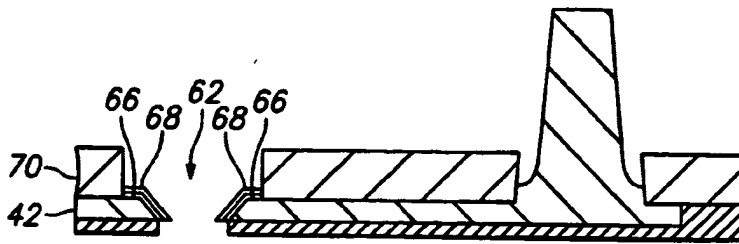


FIG. 1N

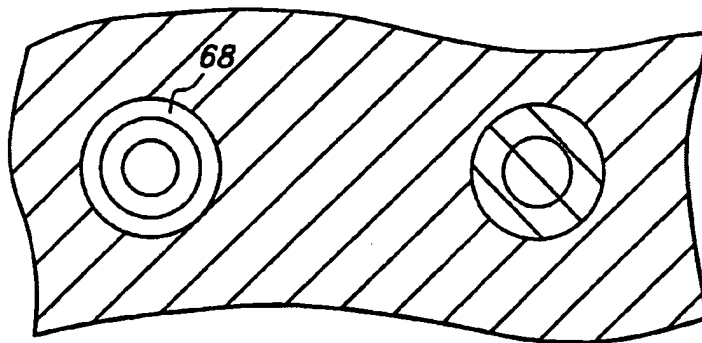


FIG. 2N

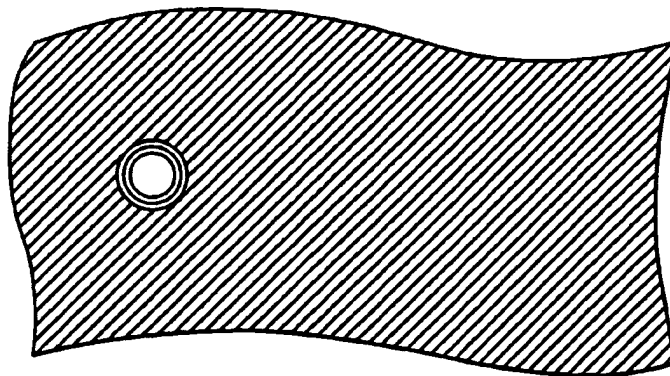


FIG. 3N

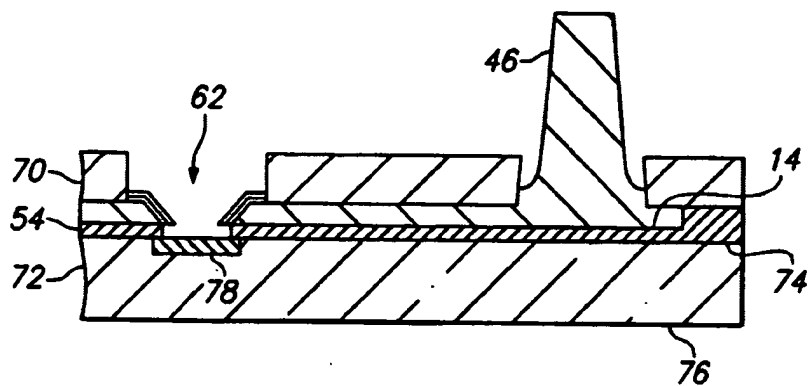


FIG. 10

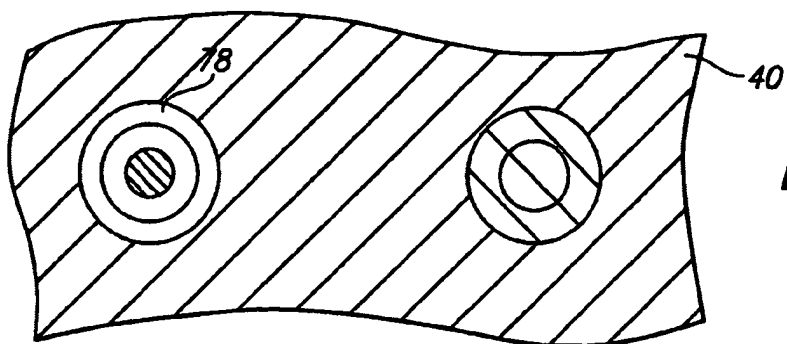


FIG. 20

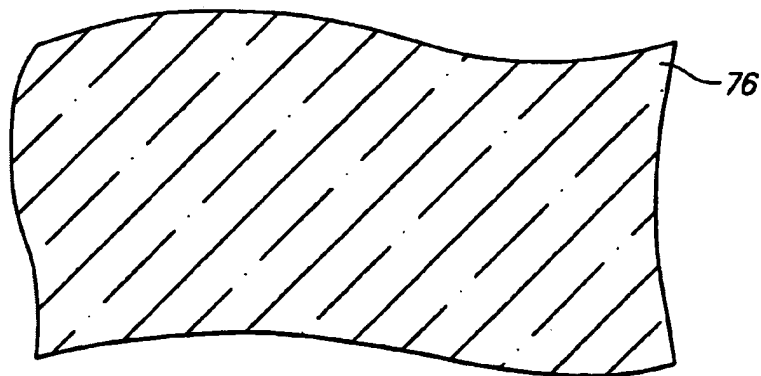


FIG. 30

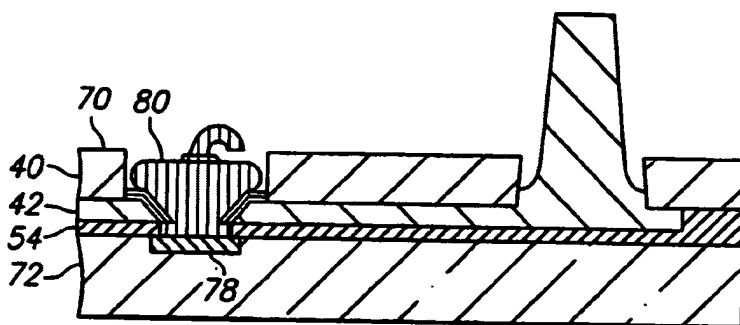


FIG. 1P

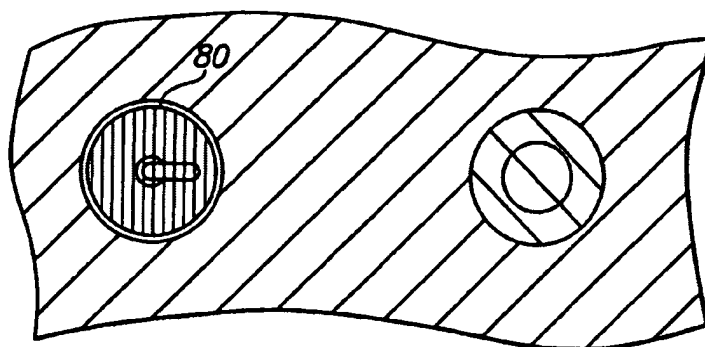


FIG. 2P

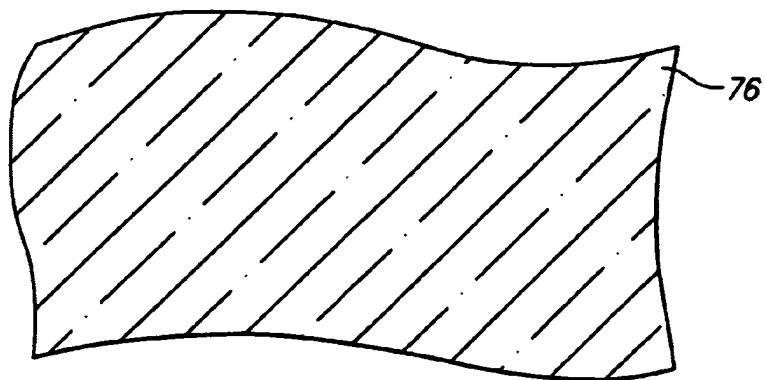


FIG. 3P

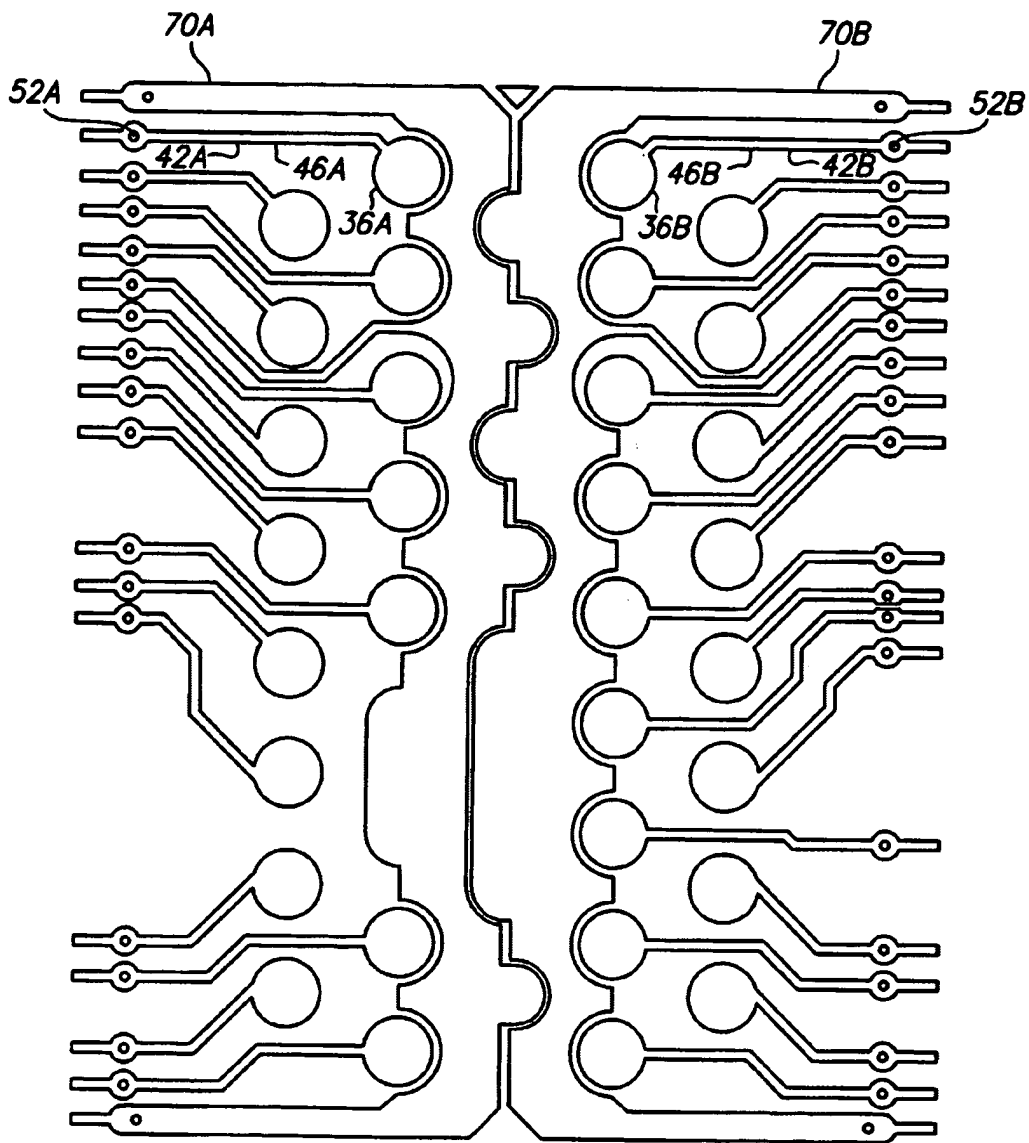


FIG. 4